

Characterization of pseudodynamic elements

Abstract

The experimental results of approbation of pseudodynamic logic elements characterization procedure are presented. These elements form a basis for implementing self-timed computing device, which performs division and square root extraction in accordance with IEEE 754 Standard. A software tool formatting the characterization results is described. It provides generation of conventional files with functional and simulation models for new developed library elements on base of characterization results. An efficiency of suggested software tool is proved while designing self-timed computing device.